



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Gp/2187
#7/13
pay
1-13-03

Application No.: 09/239,659
Filed: January 29, 1999
Inventor(s):
Thomas A. Dye, Manuel J.
Alvarez II, and Peter Geiger

Examiner: Kim, Hong Chong
Group/Art Unit: 2187
Atty. Dkt. No: 5143-01700

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, Washington, DC 20231, on the date indicated below.

Jeffrey C. Hood

Jeffrey C. Hood
Signature

1/3/2003
Date

Title: Bandwidth Reducing
Memory Controller
Including Scalable
Embedded Parallel Data
Compression and
Decompression Engines

RESPONSE TO OFFICE ACTION OF
AUGUST 5, 2002

Commissioner for Patents
Washington, D.C. 20231

RECEIVED
JAN 09 2003
Technology Center 2100

Dear Sir:

This paper is submitted in response to the Office Action of August 5, 2002, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.

IN THE TITLE:

Please delete the current title and replace with:

“Selective Lossless, Lossy, or No Compression of Data Based on Address Range,

01/14/2003 PYOUNG1 00000001 501505 09239659
Data Type, and/or Requesting Agent”

01 FC:1251 110.00 CH

IN THE CLAIMS: